FMC — The Ferroelectric Memory Company

WE'RE HIRING!

Senior Digital IC Design Engineer (m/f/div)

FMC was founded in 2016 as spin-off from Technical University Dresden. We are an innovative and ambitious semiconductor startup in the field of embedded non-volatile (eNVM) memory solutions.

With our novel and ground-breaking memory technology, based on ferroelectrics, we serve the needs of the rapidly growing IoT market for current and future technology nodes.

To complement our design team, we're looking for a Senior Digital IC Design Engineer (m/f/d) in **Dresden**.

Your future job:

- Develop ICs for our international customers
- Implement an RTL block level design for embedded memories
- Define RTL synthesis and timing constraints
- Support the full Macro Product and block level functional verification
- Equivalence checking and analyze Static Timing
- Create a behavioral model for the full Macro Product

Your profile:

- Engineering master's degree or diploma
- Min. 5 years of experience in digital design
- Good knowledge of hardware description languages (VHDL, Verilog) and System Verilog
- Understanding of digital design flow including RTL simulation, logic synthesis, timing constraints, timing closure, STA and back annotation
- Strong problem-solving ability
- Interest in working in a start-up environment

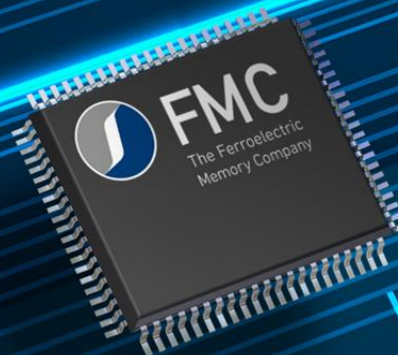**Planned start date: October 2019**

What to expect:

Flat hierarchies, possibilities for personal and technical career growth, regular team events, playroom, language courses, snacks and drinks and a competitive salary in a semiconductor startup environment

Contact us:

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www.ferroelectric-memory.com

Low power
High speed
Low cost